



Case Docket No. 58268.00000

Date February 17, 2006

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In re the Application of:

Govind MALALUR et al.

Art Unit: 2661

Application No.: 09/841,189

Examiner: Brian D. Nguyen

Filed: April 25, 2001

Confirmation No.: 3654

For: GIGABIT SWITCH ON CHIP ARCHITECTURE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Transmitted herewith is an Amendment in the above-identified application.

- ☐ Small entity status of this application under 37 CFR 1.9 and 1.27 has been established by a statement previously submitted.
- ☐ Applicant qualifies for small entity status.
- ☐ No additional fee is required.

The fee has been calculated as shown below:

					SMALL ENTITY		OTHER THAN A SMALL ENTITY	
(Col. 1)	CLAIMS REMAINING AFTER AMENDMENT	(Col. 2)	HIGHEST NO. PREVIOUSLY PAID FOR	(Col. 3)	RATE	ADDIT. FEE	RATE	ADDIT. FEE
TOTAL	33	MINUS	32	1	X25=		or X50=	50.00
INDEP.	3	MINUS	8	5	X100=		or X200=	1000.00
<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM					+180=		or +360=	
					TOTAL		or	1050.00

- ☒ Enclosed is a check in the amount of One Thousand and Fifty Dollars (\$1,050.00). Except as otherwise noted herein, the Commissioner is hereby authorized to charge payment of any other fees that may be required to complete this filing, or to credit any overpayment, to Counsel's Deposit Account No. 50-2222.
- ☐ The Commissioner is hereby authorized to charge payment for the following fees associated with this communication or credit any overpayment to Counsel's Deposit Account No. 50-2222. A duplicate copy is enclosed.
- ☒ Any filing fees required under 37 CFR 1.16.

Respectfully submitted,



Peter C. Flanagan  
Registration No. 58,178

Atty. Docket No. 58268.00000

**Customer Number 32294**

SQUIRE, SANDERS & DEMPSEY LLP

14<sup>TH</sup> Floor

8000 Towers Crescent Drive

Tysons Corner, Virginia 22182-2700

Telephone: 703-720-7800

Fax: 703-720-7802

PCF:kmp

Enclosures: Amendment  
Check No. 14078



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Confirmation No.: 3654

Govind MALALUR et al.

Art Unit: 2661

Application No.: 09/841,189

Examiner: Brian D. Nguyen

Filed: April 25, 2001

Attorney Dkt. No.: 58268.00000

For: GIGABIT SWITCH ON CHIP ARCHITECTURE

**RESPONSE UNDER 37 CFR § 1.116**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

February 17, 2005

Sir:

In response to the Office Action dated January 12, 2006, please amend the above-identified application as set forth below.

**Amendments to the claims are submitted on page 2.**

**Remarks are submitted on page 19.**

02/21/2006 JADD01 00000030 09841189

01 FC:1201  
02 FC:1202

1000.00 OP  
50.00 OP